

## Refine Search

### Search Results -

Terms	Documents
((interrupt adj5 (controller or server or processor)) or (presentation adj3 controller)) same (link\$3 near5 (loop or circular))	6

Database:

US Pre-Grant Publication Full-Text Database  
 US Patents Full-Text Database  
 US OCR Full-Text Database  
 EPO Abstracts Database  
 JPO Abstracts Database  
 Derwent World Patents Index  
 IBM Technical Disclosure Bulletins

Search:

L2

Refine Search

Recall Text

Clear

Interrupt

### Search History

 DATE: Thursday, August 18, 2005    [Printable Copy](#)    [Create Case](#)

**Set**  
**Name**    **Query**  
 side by  
 side

**Hit**  
**Count**    **Set**  
                  **Name**  
                  result set

DB=PGPB,USPT,USOC; PLUR=YES; OP=OR

L2    ((interrupt adj5 (controller or server or processor)) or (presentation adj3 controller)) same (link\$3 near5 (loop or circular))

6    L2

L1    ((interrupt adj5 (controller or server)) or (presentation adj3 controller)) same (link\$3 near5 (loop or circular))

1    L1

END OF SEARCH HISTORY

Refine Search

Search Results -

Terms	Documents
(interrupt adj5 (controller or server)) same (linked adj5 (loop or "circular list"))	1

Database:

- US Pre-Grant Publication Full-Text Database
- US Patents Full-Text Database
- US OCR Full-Text Database
- EPO Abstracts Database
- JPO Abstracts Database
- Derwent World Patents Index
- IBM Technical Disclosure Bulletins

Search:

L2

Refine Search

Recall Text

Clear

Interrupt

Search History

DATE: Thursday, August 18, 2005   [Printable Copy](#)   [Create Case](#)

Set Name Query  
side by side

Hit Count Set Name  
result set

DB=PGPB,USPT,USOC; PLUR=YES; OP=OR

<u>L2</u>	(interrupt adj5 (controller or server)) same (linked adj5 (loop or "circular list"))	1	<u>L2</u>
<u>L1</u>	(interrupt adj3 (controller or server)) same (linked adj5 (loop or "circular list"))	1	<u>L1</u>

END OF SEARCH HISTORY

## Refine Search

### Search Results -

Terms	Documents
(interrupt adj5 (controller or server)) same (linked adj5 (loop or "circular list"))	1

Database:

US Pre-Grant Publication Full-Text Database  
 US Patents Full-Text Database  
 US OCR Full-Text Database  
 EPO Abstracts Database  
 JPO Abstracts Database  
 Derwent World Patents Index  
 IBM Technical Disclosure Bulletins

Search:

L3

Refine Search

Recall Text

Clear

Interrupt

### Search History

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#### Set Name Query

side by side

#### Hit Count Set Name

result set

DB=EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR

L3   (interrupt adj5 (controller or server)) same (linked adj5 (loop or "circular list"))

1   L3

DB=PGPB,USPT,USOC; PLUR=YES; OP=OR

L2   (interrupt adj5 (controller or server)) same (linked adj5 (loop or "circular list"))

1   L2

L1   (interrupt adj3 (controller or server)) same (linked adj5 (loop or "circular list"))

1   L1

END OF SEARCH HISTORY

Refine Search

Search Results -

Terms	Documents
(710/48  710/244  710/260  710/261  710/262  710/263  710/264  710/265  710/266  710/269  712/40).ccls.	2188

Database:

- US Pre-Grant Publication Full-Text Database
- US Patents Full-Text Database
- US OCR Full-Text Database
- EPO Abstracts Database
- JPO Abstracts Database
- Derwent World Patents Index
- IBM Technical Disclosure Bulletins

Search:

L1

Refine Search

Recall Text

Clear

Interrupt

Search History

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**Set Name Query**  
side by side

**Hit Count Set Name**  
result set

DB=PGPB,USPT,USOC; PLUR=YES; OP=OR  
L1    710/48,244,260-265,266,269;712/40.ccls.    2188    L1

END OF SEARCH HISTORY

## Refine Search

### Search Results -

Terms	Documents
L1 and L2	1

Database:

US Pre-Grant Publication Full-Text Database  
 US Patents Full-Text Database  
 US OCR Full-Text Database

EPO Abstracts Database  
 JPO Abstracts Database  
 Derwent World Patents Index  
 IBM Technical Disclosure Bulletins

Search:

L3

Refine Search

Recall Text



Clear

Interrupt

### Search History

DATE: Thursday, August 18, 2005   [Printable Copy](#)   [Create Case](#)

<u>Set</u> <u>Name</u> side by side	<u>Query</u>	<u>Hit</u> <u>Count</u>	<u>Set</u> <u>Name</u> result set
<u>L3</u>	DB=PGPB,USPT,USOC; PLUR=YES; OP=OR l1 and L2	1	<u>L3</u>
<u>L2</u>	((interrupt adj5 (controller or server or process\$3)) or (presentation adj3 controller)) same (link\$3 near5 (loop or circular))	12	<u>L2</u>
<u>L1</u>	710/48,244,260-265,266,269;712/40.ccls.	2188	<u>L1</u>

END OF SEARCH HISTORY





**EAST - [Untitled1:1]**

File View Edit Tools Window Help

☐ Drafts  
☐ Pending  
☒ Active  
☐ Failed  
☐ Saved  
☐ Favorites  
☐ Tagged (0)  
☐ UDC  
☐ Queue  
☐ Trash

Search

DBs:  ☒ Plurals  
 Default operator:  ☒ Highlight all hit terms initially

L1: (0) ((interrupt adj  
 L2: (363) ((interrupt a  
 L3: (345) ((interrupt a  
 L4: (0) ((interrupt ad  
 L5: (18) ((interrupt a

((interrupt adj5 (controller or server)) or  
 (presentation adj3 controller)) same (link\$3  
 same (loop or circular))

	U	I	Document ID	Issue Dat	Pages	Title	Current OR	Current X
1	<input type="checkbox"/>	<input type="checkbox"/>	US 6597727 B2	20030722	62	Programmable modem apparatus for transmitt	375/147	375/222; 375/316
2	<input type="checkbox"/>	<input type="checkbox"/>	US 6549979 B1	20030415	43	Address mapping in mass storage device mounting	711/114	711/202; 711/220
3	<input type="checkbox"/>	<input type="checkbox"/>	US 6529589 B1	20030304	15	Method and system for monitoring and controll	379/102.01	340/310.0
4	<input type="checkbox"/>	<input type="checkbox"/>	US 6233643 B1	20010515	13	Apparatus including a host processor and comm	710/316	370/362; 370/463;
5	<input type="checkbox"/>	<input type="checkbox"/>	US 5968158 A	19991019	13	Apparatus including a host processor and comm	710/260	370/280; 370/311;
6	<input type="checkbox"/>	<input type="checkbox"/>	US 4675807 A	19870623	15	Multiple file transfer to streaming device	710/5	
7	<input type="checkbox"/>	<input type="checkbox"/>	US 4625730 A	19861202	12	Patient ECG recording control for an automati	607/4	128/903; 607/27;
8	<input type="checkbox"/>	<input type="checkbox"/>	US 4535453 A	19850813	38	Signaling input/output processing module for a	370/384	
9	<input type="checkbox"/>	<input type="checkbox"/>	US 4314334 A	19820202	100	Serial data communication system ha	710/3	710/29
10	<input type="checkbox"/>	<input type="checkbox"/>	US 4306803 A	19811222	99	Microprocessor and control apparatus in a	399/77	700/2
11	<input type="checkbox"/>	<input type="checkbox"/>	US 4283773	19810811	99	Programmable master	700/3	709/208



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Search Results

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SEARCH

IEEE Xplore GUIDE

SUPPORT

Results for "( interrupt and controller&lt;in&gt;metadata ) &lt;and&gt; ( link&lt;in&gt;metadata ) and (loop or ..."

Your search matched 22 of 1225093 documents.

A maximum of 100 results are displayed, 25 to a page, sorted by Relevance in Descending order.

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Modify Search

☐ Check to search only within this results set

 Display Format:
 ☒ Citation
 ☐ Citation & Abstract

## x Key

IEEE JNL IEEE Journal or Magazine

IEEE JNL IEEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEEE CNF IEEE Conference Proceeding

IEEE STD IEEE Standard

Select Article Information

- ☐
**1. Peripheral board controller for digital exchange systems**  
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- ☐ **18. Fault-tolerant control system of flexible arm for sensor fault by using reaction force observer**  
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## Peripheral interface standards for microprocessors

Nilsoud, J.D.

This paper appears in: **Proceedings of the IEEE**  
Publication Date: June 1976  
Volume: 64 , Issue: 6  
On page(s): 896 - 904  
ISSN: 0018-9219  
Posted online: 2006-06-28 14:50:20.0

### Abstract

Interfacing a peripheral to a microprocessor requires a peripheral interface attached to the processor and a controller within the peripheral, with usually a transmission link in between. Universal and special interface circuits are becoming available for an easy interface with the various microprocessor peripherals. The use of programmable interfaces increases the flexibility of the interface, and standards are necessary for reaching high-volume production. Present standards include serial EIA RS232 and the recent Hewlett-Packard Interface Bus for measuring instruments. Four new standards developed and used at the Mini and Microcomputer Laboratory of Lausanne fill a gap at the following levels: 1) microprocessor data, address, and control bus, 2) parallel data transfer, 3) simple serial data transfer with handshake, and 4) serial bus for data exchange between terminals and processors.

Index Terms

Inspec

Controlled Indexing

Not Available

Non-controlled Indexing

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Author Keywords

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References

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